COMMUNCIATIONS: Direct Digital Synthesis

32 BIT DDS	S																		
DDS FUN	CTIONS																		
	(01101)								SFDR	SFDR	SFDR					Mode	l Desi	gnator	
		Power S	Supply						Narrow	Narrow	Wide	INL	DNL	Volta	age	Temp		5114101	
			Requirements		Update	SNR		THD		Band 200KHZ				Reference		Range		#	Price
	#	+Vcc	+Icc	Output Current	Rate	@ Fmax	@ Fout		-dBc	-dBc	-dBc	Lsb's	Lsb's	INT	EXT	0	-40	of	
MODEL	Bits	+ Volts	+ mA	mA	MSPS	dB	MHz	-dB	CLK=6.2MHZ	CLK=6.2MHZ	CLK=6.2MHZ	+25C	+25C	Volts	Volts	70	85	Pins	/100's
											Fout=2.11MHz								
AD7008 (C	omplete 32 l	oit DDS	with O	uadratur	e Modul:	ator)													
1127000 (0	inpiece 52 k		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		0 1/10441														
AD7008-20	10	+5V	160	20	20	50	2	55	70	ns	55	±1	±1	1.27		JP	AP	44	\$22.50
AD7008-50	10	+5V	160		50	50	2	53	70	ns	55	±1	±1			JP	AP	44	\$27.50
									SFDR	SFDR	SFDR					Mode	l Desi	gnator	
		Power S	Supply						Narrow	Narrow	Wide	INL	DNL	Volta	age	Temp			
		Requirements		Output	Update	Update SNR		THD	Band 50KHZ	Band 200KHZ	Band 2MHZ	Z		Reference		Range		#	Price
	#	+Vcc	+Icc	Current	Rate	@ Fmax	@ Fout	t	-dBc	-dBc	-dBc	Lsb's	Lsb's	INT	EXT	0	-40	of	
MODEL	Bits	+ Volts	+ mA	mA	MSPS	dB	MHz	-dB	CLK=6.2MHZ	CLK=6.2MHZ	CLK=6.2MHZ	+25C	+25C	Volts	Volts	70	85	Pins	/100's
									Fout=2.11MHz	Fout=2.11MHz	Fout=2.11MHz								
AD983x (C	omplete 32 l	oit DDS	with F	requency	and Pha	se Modu	lators,	Sine l	Lookup)										
AD9830	10	+5V	60	20	50	50	2	53	72	68	50	±1	±0.5	1.21			Α	48	\$11.70
AD9831	10	+3V	12	4	25	50	1	55	-70 @±25KHz	-55 @±1MHz	-55 @±1MHz	±1	±1	1.21			A	48	\$7.35
AD9832	10	+3V	12	4	25	50	1	55	-70 @±25KHz	-55 @±1MHz	-55 @±1MHz	±1	±1	1.21			Α	16	\$7.35
AD9835	10	+5V	40	4	50	50	1	53	-72 @±50KHz		-50 @±2MHz	±1	±1	1.16			A	16	
									SFDR	SFDR	SFDR					Mode	l Desi	gnator	
		Power S	Power Supply					,-	Narrow	Narrow	Wide	INL	DNL	Voltage		Temp			
		Requirements		Output	Update	SNR	THD		Band 50KHZ	Band 200KHZ	Band 2MHZ			Reference		Range		#	Price
	#	+Vcc	+Icc	Current	Rate	@ Fmax	@ Fout		-dBc	-dBc	-dBc	Lsb's	Lsb's	INT	EXT	0	-40	of	
MODEL	Bits	+ Volts	+ mA	mA	MSPS	dB	MHz	-dB	Fout 40 1MHz	Fort 40 1MHz	Fout=40.1MHz	+25C	+25C	Volts	Volts	70	85	Pins	/100's
DDS/DAC	 Clock Gener	rator w/	On Chi	n Compa	rator				rout=40.1MHZ	rout=40.1MHZ	rout=40.1MHZ								
AD9850	10	+3V	56	20 20	110	NS			NS	NS	NS	NS	NS	1.21			Α	28	\$14.55
AD9850	10	+5V	76	20	125	NS			80	77	46	±1/2	±1/2	1.21			A	28	\$14.55
1117030	10	TJV	70	20	149	140			gu	11	40	11/2	11/2	1.41			Λ	20	Ψ14.33

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